

Fig. 2 (A)

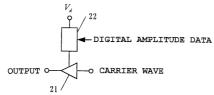


Fig. 2 (B) digital amplitude data \downarrow 23

Fig. 2 (C)

DIGITAL AMPLITUDE DATA

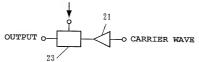


Fig. 2 (D)

DIGITAL AMPLITUDE DATA

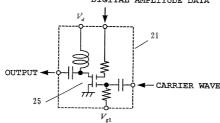


Fig. 3 (A)

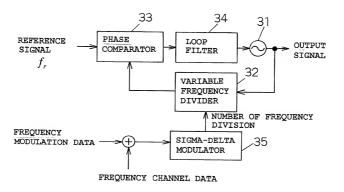


Fig. 3 (B)

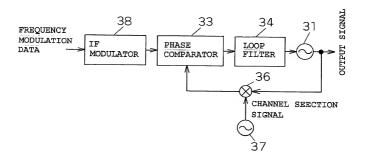


Fig. 4 (A)

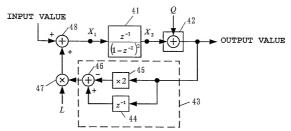
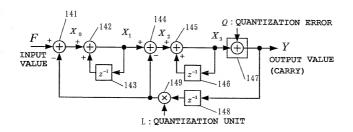


Fig. 4 (B) $X_1 \xrightarrow{51} 52 \xrightarrow{53} X_2$



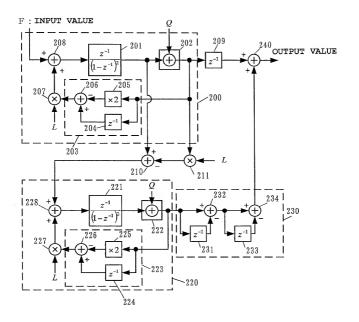
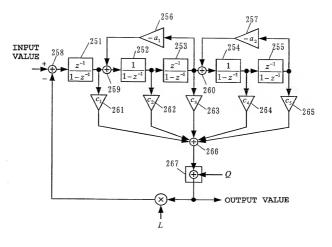
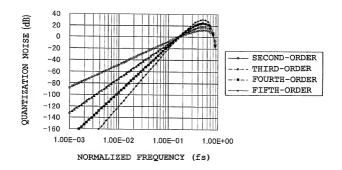


Fig. 7





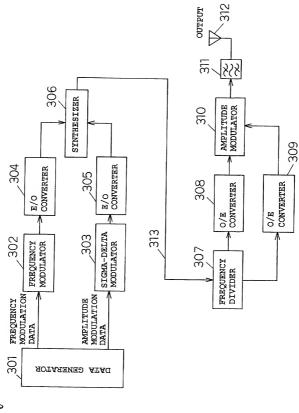


Fig. 9

Fig. 10

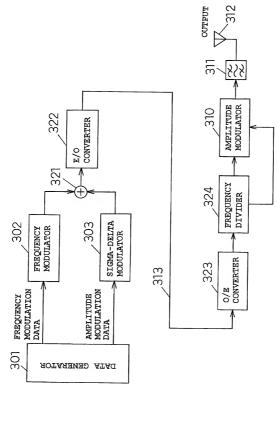


Fig. 11 (A)

OUTPUT SIGNAL OF FREQUENCY MODULATOR



Fig. 11 (B)

AMPLITUDE MODULATION DATA

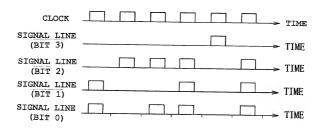
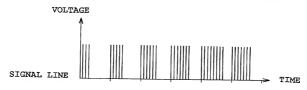


Fig. 11 (C)

OUTPUT SIGNAL OF SIGMA-DELTA MODULATOR



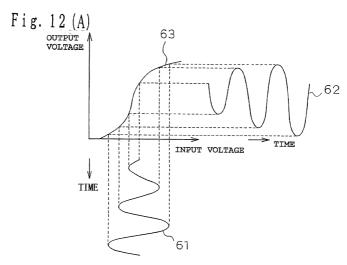


Fig. 12 (B)

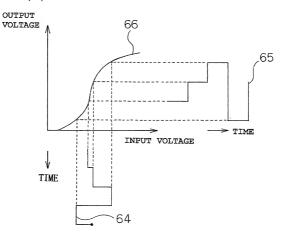


Fig. 13

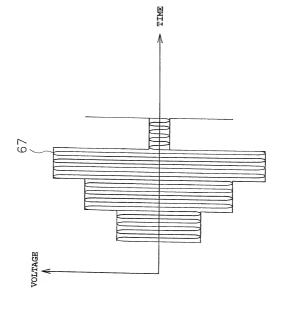


Fig. 14

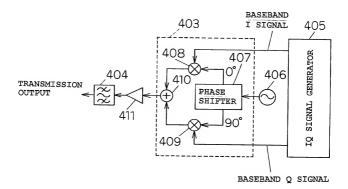


Fig. 15

